



USPTO/JPO BILATERAL MEETING ON INDUSTRIAL DESIGNS

Organized by
the United States Patent and Trademark Office (USPTO)
Alexandria, Virginia, USA
March 24-25, 2011

AGENDA

**Location: Global Intellectual Property Academy
James Madison Building, East
Paris Conference Room
United States Patent and Trademark Office
James Madison Building,
600 Dulany Street
Alexandria, Virginia 22314, USA**

(Unless otherwise noted, all sessions will be held in this location)

Thursday, March 24

- 10:00 am Meet at Madison Building, Atrium Lobby
- 10:10 – 10:25 am Welcome and Introductions
Robert Olszewski
Director, Technology Center 2900
- 10:25 – 10:45 am Presentation: Current Status JPO Design Registration
System and Examination Standards

- 10:45 – 11:00 am Presentation: USPTO Design Operations Update
Robert Olszewski
Director, Technology Center 2900
- 11:50 – 11:20 am Presentation: JPO Design Classification System
- 11:20 – 12:05 am Presentation: Overview of USPTO Design Classification System
Patricia White
International Liaison Staff
- 12:05 – 1:30 pm Lunch Break
- 1:30 – 2:15 pm Presentation: USPTO Design Reclassification Projects – Recent and Planned
Caron Veynar
Supervisory Patent Examiner, Group Art Unit 2913
- 2:15 – 2:45 USPTO/JPO Discussion: classification framework for packages, writing instruments, and cell phone covers and skins
- 2:45 - 3:05 pm Presentation: Evaluating Sufficiency of Application Disclosure at JPO
- 3:05 – 3:20 pm Break

3:20 – 3:50 pm Presentation: Evaluating Sufficiency of Application
Disclosure at USPTO
Joel Sincavage
Design Practice Specialist, Technology Center 2900

3:50 – 4:50 pm Case Studies: Evaluating Sufficiency of Application
Disclosure

Friday, March 25

10:00 – 10:20 am Presentation: Evaluating Unity of Design at JPO

10:20 – 10:50 am Presentation: Obviousness Requirement and USPTO
Design Restriction Practice
Celia Murphy
Supervisory Patent Examiner, Group Art Unit 2914

10:50 – 11:30 am Case Studies: USPTO Design Restriction Practice

11:30 – 12:20 pm East Search Demonstration
Celia Murphy
Supervisory Patent Examiner, Group Art Unit 2914

12:20 – 2:00 pm Lunch Break

2:00 pm Meet at Remsen Building, Lobby

- 2:05 – 2:50 pm Design Examination Floor Tour and Examination Procedures
Demonstration
Celia Murphy
Supervisory Patent Examiner, Group Art Unit 2914
Remsen Building, 5th Floor
- 2:50 – 3:35 USPTO Examination Standards: Prior Art and Other Issues
Joel Sincavage
Design Practice Specialist, Technology Center 2900
- 3:35 – 4:05 pm USPTO/JPO Discussion: Final Question and Answer
Period, Program Wrap-Up, and Items for Future Discussions

[15 minute breaks to be included in morning and afternoon Friday as needed]