

1	<b>SUPERCONDUCTOR (E.G., CRYOGENIC, ETC.)</b>	37	<b>MULTIFUNCTIONAL OR PROGRAMMABLE (E.G., UNIVERSAL, ETC.)</b>
2	.Tunneling device	38	.Having details of setting or programming of interconnections or logic functions
3	..Josephson tunneling device		
4	...Plural devices (e.g., distributive device, etc.)	39	.Array (e.g., PLA, PAL, PLD, etc.)
5	...Interference device (i.e., SQUID)	40	..With flip-flop or sequential device
6	..Function of AND, OR, NAND, NOR, or NOT	41	..Significant integrated structure, layout, or layout interconnections
7	.Function of AND, OR, NAND, NOR, or NOT	42	..Bipolar transistor
8	<b>SECURITY (E.G., ACCESS OR COPY PREVENTION, ETC.)</b>	43	...Emitter-coupled logic or emitter-follower logic
9	<b>RELIABILITY</b>	44	..Field effect transistor
10	.Redundant	45	...Complementary FET`s
11	..Voter circuit (e.g., majority logic, etc.)	46	.Sequential (i.e., finite state machine) or with flip-flop
12	..With flip-flop	47	.Significant integrated structure, layout, or layout interconnections
13	..With field effect-transistor	48	.Bipolar transistor
14	.Fail-safe	49	.Field-effect transistor
15	.Parasitic prevention in integrated circuit structure	50	..Complementary FET`s
16	<b>WITH TEST FACILITATING FEATURE ACCELERATING SWITCHING</b>	51	<b>INHIBITOR</b>
17	.Bipolar transistor	52	<b>EXCLUSIVE FUNCTION (E.G., EXCLUSIVE OR, ETC.)</b>
18	..With Schottky device	53	.Half-adder or quarter-adder
19	...Complementary transistors	54	.Exclusive NOR
20		55	.With field-effect transistor
21	<b>SIGNAL SENSITIVITY OR TRANSMISSION INTEGRITY</b>	56	<b>TRI-STATE (I.E., HIGH IMPEDANCE AS THIRD STATE)</b>
22	.Input noise margin enhancement	57	.With field effect-transistor
23	..With field effect-transistor	58	..Complementary FET`s
24	...Complementary FET`s	59	<b>THREE OR MORE ACTIVE LEVELS (E.G., TERNARY, QUATERNARY, ETC.)</b>
25	...Depletion or enhancement	60	.With conversion (e.g., three level to two level, etc.)
26	.Output switching noise reduction	61	<b>INSULATED GATE CHARGE TRANSFER DEVICE</b>
27	..With field effect-transistor	62	<b>INTERFACE (E.G., CURRENT DRIVE, LEVEL SHIFT, ETC.)</b>
28	...With clocking	63	.Logic level shifting (i.e., interface between devices of different logic families)
29	.Pulse shaping (e.g., squaring, etc.)	64	..Bi-CMOS
30	.Bus or line termination (e.g., clamping, impedance matching, etc.)	65	...TTL to/from CMOS
31	.Signal level or switching threshold stabilization	66	...ECL to/from CMOS
32	..Temperature compensation	67	...ECL to/from TTL
33	..Bias or power supply level stabilization	68	..Field-effect transistor (e.g., JFET, MOSFET, etc.)
34	..With field effect-transistor		
35	<b>THRESHOLD (E.G., MAJORITY, MINORITY, OR WEIGHTED INPUTS, ETC.)</b>		
36	.With field effect-transistor		

69	...ECL to/from GaAs FET (e.g., MESFET, etc.)	104	<b>FUNCTION OF AND, OR, NAND, NOR, OR NOT</b>
70	...TTL to/from MOS	105	.Decoding
71	...TTL to/from CMOS	106	..With field-effect transistor
72	....Using depletion or enhancement transistors	107	...Depletion or enhancement
73	...ECL to/from MOS	108	...CMOS
74	...ECL to/from TTL	109	.Bipolar and FET
75	..Bipolar transistor	110	..Bi-CMOS
76	...TTL to/from MOS	111	.Space discharge device (e.g., vacuum tube, etc.)
77	...ECL to/from MOS	112	.Field-effect transistor (e.g., JFET, etc.)
78	...ECL to/from TTL	113	..Pass transistor logic or transmission gate logic
79	..Integrated Injection Logic (I <sup>2</sup> L)	114	..Wired logic (e.g., wired-OR, wired-AND, dotted logic, etc.)
80	.Supply voltage level shifting (i.e., interface between devices of a same logic family with different operating voltage levels)	115	..Source-coupled logic (e.g., current mode logic (CML), differential current switch logic (DCSL), etc.)
81	..CMOS	116	..Schottky-gate FET (i.e., MESFET)
82	.Current driving (e.g., fan in/out, off chip driving, etc.)	117	...Depletion or enhancement
83	..Field-effect transistor	118	...Diode transistor logic
84	...Bi-CMOS	119	..MOSFET (i.e., metal-oxide semiconductor field-effect transistor)
85	....Having plural output pull-up or pull-down transistors	120	...Depletion or enhancement
86	..Bus driving	121	...CMOS
87	..Having plural output pull-up or pull-down transistors	122	..Complementary FET`s
88	...With capacitive or inductive bootstrapping	123	..With semiconductor diode or negative resistance device
89	..Bipolar transistor	124	.Bipolar transistor (e.g., RTL, DCTL, etc.)
90	...Bus driving	125	..Wired logic or open collector logic (e.g., wired-OR, wired-AND, dotted logic, etc.)
91	..Having plural output pull-up or pull-down transistors	126	..Emitter-coupled or emitter-follower logic
92	..With capacitive or inductive bootstrapping	127	...Current mode logic (CML)
93	<b>CLOCKING OR SYNCHRONIZING OF LOGIC STAGES OR GATES</b>	128	..Transistor-transistor logic (TTL)
94	.Metastable state prevention	129	...Complementary transistor logic (CTL)
95	.Field-effect transistor	130	..Diode-transistor logic (DTL)
96	..Two or more clocks (e.g., phase clocking, etc.)	131	...With metal semiconductor junction diode (e.g., Schottky barrier, etc.)
97	...MOSFET	132	..With negative resistance device (e.g., tunnel diode, thyristor, etc.)
98	..MOSFET	133	.Diode
99	<b>HAVING LOGIC LEVELS CONVEYED BY SIGNAL FREQUENCY OR PHASE</b>	134	..Negative resistance diode (e.g., tunnel, gunn, etc.)
100	<b>INTEGRATED INJECTION LOGIC</b>		
101	<b>SIGNIFICANT INTEGRATED STRUCTURE, LAYOUT, OR LAYOUT INTERCONNECTIONS</b>		
102	.Field-effect transistor		
103	..Complementary FET`s		

135 .Negative resistance device  
136 **MISCELLANEOUS**

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