CPC COOPERATIVE PATENT CLASSIFICATION

G PHYSICS

(NOTES omitted)

INSTRUMENTS

G09 EDUCATION; CRYPTOGRAPHY; DISPLAY; ADVERTISING; SEALS

G09G ARRANGEMENTS OR CIRCUITS FOR CONTROL OF INDICATING DEVICES USING STATIC MEANS TO PRESENT VARIABLE INFORMATION (arrangements for transferring data between digital computers and displays <u>G06F 3/14</u>; static indicating arrangements comprising an association of a number of separate sources or light control cells <u>G09F 9/00</u>; static indicating arrangements comprising integral associations of a number of light sources <u>H01J</u>, <u>H01K</u>, <u>H01L</u>, <u>H05B 33/12</u>; scanning, transmission or reproduction of documents or the like, e.g. facsimile transmission, details thereof <u>H04N 1/00</u>)

<u>NOTES</u>

- 1. This subclass <u>covers</u> indicator consoles, i.e. arrangements or circuits for processing control signals to achieve the display, e.g. for the calling up, reception, storage, regeneration, coding, decoding, addressing of control signals.
- This subclass <u>does not cover</u> the structural details of the indicating devices, such as panels or tubes <u>per se</u>, or assemblies of individual light sources, which are covered by the relevant subclasses, e.g. <u>H01J</u>, <u>H01K</u>, <u>H01L</u>, <u>H10K</u>, <u>G02F</u>, <u>G09F</u>, <u>H05B</u>.
- 3. Contrary to subclass <u>H04N</u>, in which are classified display devices capable of representing continuous brightness value scales, this subclass is limited to devices using only a discrete number of brightness values, e.g. visible/non-visible.
- 4. The visual effect may be produced by a luminescent screen scanned by an electron beam, directly by controlled light sources, by projection of light, from controlled light sources onto characters, symbols, or elements thereof drawn on a support, or by electric, magnetic, or acoustic control of the parameters of light rays from an independent source.

WARNING

In this subclass non-limiting references (in the sense of paragraph 39 of the Guide to the IPC) may still be displayed in the scheme.

1/00	Control arrangements or circuits, of interest only in connection with cathode-ray tube indicators; {General aspects or details, e.g. selection emphasis on particular characters, dashed line or dotted
	line generation; Preprocessing of data}(cathode-
	ray oscilloscopes <u>G01R 13/20;</u> {radar display
	arrangements G01S 7/04; display of digital non-
	picture data in television systems <u>H04N 7/0255</u> })
1/002	• {Intensity circuits (<u>G09G 1/06</u> - <u>G09G 1/28</u> take
	precedence)}
1/005	• {Power supply circuits}
1/007	• {Circuits for displaying split screens}
1/02	• Storage circuits (<u>G09G 1/06</u> - <u>G09G 1/28</u> take precedence)
1/04	• Deflection circuits {; Constructional details
	not otherwise provided for (electron-optical
	arrangements H01J 29/46, H01J 37/04,
	<u>H01J 37/302</u>)
1/06	• using single beam tubes (<u>G09G 1/26</u> , <u>G09G 1/28</u>
	take precedence), {e.g. three-dimensional or
	perspective representation, rotation or translation of
	display pattern, hidden lines, shadows (G09G 1/28
	takes precedence; stereoscopic TV-systems,
	details thereof H04N 13/00; oscilloscopes for
	three-dimensional representation G01R 13/206;
	vectorscopes <u>G01R 13/208</u>)}

1/07	 with combined raster scan and calligraphic
	display

1/08	• the beam directly tracing characters, the
	information to be displayed controlling the
	deflection {and the intensity} as a function of
	time in two spatial co-ordinates, e.g. according to
	a cartesian co-ordinate system
1/10	

- 1/10 . . . the deflection signals being produced by essentially digital means, e.g. incrementally
 1/12 . . . the deflection signals being produced by
- essentially analogue means
 1/14 . the beam tracing a pattern independent of the information to be displayed, this latter determining the parts of the pattern rendered respectively visible and invisible
- 1/143 • {Circuits for displaying horizontal and vertical lines}
- 1/146. . . {Flicker reduction circuits}1/16. . . the pattern of rectangular co-ordinates
 - extending over the whole area of the screen, i.e. television type raster

1/162	•••• { for displaying digital inputs as analog magnitudes, e.g. curves, bar graphs, coordinate axes, singly or in combination with alpha-numeric characters (cathode-ray oscilloscopes for displaying analog inputs, singly or in combination with alpha-numeric characters <u>G01R 13/20</u> ; television receiver circuitry for displaying supplementary, e.g. alpha-numeric, information <u>H04N 5/445</u>) }
1/165	•••• {Details of a display terminal using a CRT, the details relating to the control arrangement of the display terminal and to the interfaces thereto (details suitable for both CRT and flat panel <u>G09G 5/003</u> ; specific for a flat panel <u>G09G 3/2092</u>)}
1/167	••••• {Details of the interface to the display terminal specific for a CRT (details suitable for both CRT and flat panel <u>G09G 5/006</u> , specific for a flat panel <u>G09G 3/2096</u>)}
1/18	a small local pattern covering only a single character, and stepping to a position for the following character, e.g. in rectangular or polar co-ordinates, or in the form of a framed star
1/20	 using multi-beam tubes (<u>G09G 1/26</u>, <u>G09G 1/28</u> take precedence)
1/22	 using tubes permitting selection of a complete character from a number of characters {(tubes therefor <u>H01J 31/16</u>)}
1/24	 using tubes permitting selection of individual elements forming in combination a character {(see provisionally also G09G 1/22)}
1/26	• using storage tubes {(tubes therefor <u>H01J 31/58</u>)}
1/28	• using colour tubes {(tubes therefor $H01J 31/20$)}
1/285	• • {Interfacing with colour displays, e.g. TV receiver}
3/00	Control arrangements or circuits, of interest only in connection with visual indicators other than cathode-ray tubes
3/001	{using specific devices not provided for in groups $\underline{G09G 3/02}$ - $\underline{G09G 3/36}$, e.g. using an intermediate record carrier such as a film slide; Projection systems; Display of non-alphanumerical information, solely or in combination with alphanumerical information, e.g. digital display on projected diapositive as background (slide projectors <u>per se G03B 23/00</u> = 42 HP)}
3/002	 {to project the image of a two-dimensional display, such as an array of light emitting or modulating elements or a CRT}
3/003	• • {to produce spatial visual effects}
3/004	• {to give the appearance of moving signs}
3/005	• {forming an image using a quickly moving array of imaging elements, causing the human eye to perceive an image which has a larger resolution than the array, e.g. an image on a cylinder formed by a rotating line of LEDs parallel to the axis of rotation}
3/006	 {Electronic inspection or testing of displays and display drivers, e.g. of LED or LCD displays (testing individual LED's <u>G01R 31/2635</u>; testing lamps <u>G01R 31/44</u>; testing of optical features of LCD displays <u>G02F 1/1309</u>)}
3/007	• {Use of pixel shift techniques, e.g. by mechanical shift of the physical pixels or by optical shift of the perceived pixels}

2/009	
3/008	• {forming an image on an image carrier by relative movement of a writing unit to the image carrier,
	e.g. on a photoconductive rotating belt, or on an
	electronic blackboard}
3/02	• by tracing or scanning a light beam on a screen
3/025	• • {with scanning or deflecting the beams in two directions or dimensions}
3/03	 {specially adapted for displays having non-planar surfaces, e.g. curved displays}
3/035	• • {for flexible display surfaces}
3/04	 for presentation of a single character by selection from a plurality of characters, or by composing the character by combination of individual elements, e.g. segments {using a combination of such display devices for composing words, rows or the like, in a
	frame with fixed character positions}
3/045	• • {Selecting complete characters}
3/06	• • using controlled light sources
3/08	using incandescent filaments
3/10	• • using gas tubes
3/12	• • using electroluminescent elements
3/14	• • • Semiconductor devices, e.g. diodes
3/16 3/18	• by control of light from an independent source
3/18	 using liquid crystals using electrochromic devices
3/19	 for presentation of an assembly of a number of
3/20	characters, e.g. a page, by composing the assembly by combination of individual elements arranged in a matrix {no fixed position being assigned to or
	needed to be assigned to the individual characters or partial characters }
3/2003	A Display of colours (specific for liquid crystal
5/2005	displays G09G 3/3607)}
3/2007	• {Display of intermediate tones}
3/2011	• • {by amplitude modulation}
3/2014	• • • {by modulation of the duration of a single pulse during which the logic level remains constant}
3/2018	• • • {by time modulation using two or more time intervals}
3/2022	• • • {using sub-frames}
3/2025	••••••••••••••••••••••••••••••••••••••
3/2029	••••• {the sub-frames having non-binary weights}
3/2033	 {with splitting one or more sub-frames corresponding to the most significant bits into two or more sub-frames}
3/2037	••••• {with specific control of sub-frames corresponding to the least significant bits}
3/204	• • • • {the sub-frames being organized in consecutive sub-frame groups}
3/2044	• • {using dithering}
3/2048	• • • • { with addition of random noise to an image
	signal or to a gradation threshold}
3/2051	•••• {with use of a spatial dither pattern}
3/2055	{the pattern being varied in time}
3/2059	{using error diffusion}
3/2062	• • • • {using error diffusion in time}
3/2066	•••• {with error diffusion in both space and time}
3/207	• • • {by domain size control (<u>G09G 3/3637</u> takes precedence)}
3/2074	• • • {using sub-pixels}

3/2077	• • • {by a combination of two or more gradation control methods}
3/2081	• • • { with combination of amplitude modulation and time modulation (space and time error diffusion <u>G09G 3/2066</u>) }
3/2085	• • {Special arrangements for addressing the individual elements of the matrix, other than by driving respective rows and columns in combination}
3/2088	• • • {with use of a plurality of processors, each processor controlling a number of individual elements of the matrix}
3/2092	• • {Details of a display terminals using a flat panel, the details relating to the control arrangement of the display terminal and to the interfaces thereto (suitable for both CRT and flat panel <u>G09G 5/003;</u> specific for a CRT <u>G09G 1/165</u>)}
3/2096	• • • {Details of the interface to the display terminal specific for a flat panel (suitable for both CRT and flat panel <u>G09G 5/006</u> ; specific for a CRT <u>G09G 1/167</u>)}
3/22	• • using controlled light sources
3/24	• • • using incandescent filaments
3/26	• • • to give the appearance of moving signs
3/28	• • • using luminous gas-discharge panels, e.g. plasma panels
3/2803	• • • {Display of gradations (<u>G09G 3/288</u> takes precedence)}
3/2807	• • • with discharge activated by high-frequency signals specially adapted therefor
3/2813	using alternating current [AC] - direct current [DC] hybrid-type panels
3/282	using DC panels
3/285	• • • • using self-scanning
3/288	using AC panels
3/29	•••••• using self-shift panels {with sequential transfer of the discharges from an input position to a further display position}
3/291	 controlling the gas discharge to control a cell condition, e.g. by means of specific pulse shapes
3/292	••••• for reset discharge, priming discharge or erase discharge occurring in a phase other than addressing
3/2922	••••• {Details of erasing}
3/2925	••••• {Details of priming}
3/2927	••••• {Details of initialising}
3/293	for address discharge
3/2932	••••••••••••••••••••••••••••••••••••••
3/2935	{Addressed by erasing selected cells that are in an ON state}
3/2937	{being addressed only once per frame}
3/294	for lighting or sustain discharge
3/2942	••••••••••••••••••••••••••••••••••••••
3/2944	•••••• {by varying the frequency of sustain pulses or the number of sustain pulses proportionally in each subfield of the whole frame }

3/2946	•••••• {by introducing variations of the frequency of sustain pulses within a frame or non-proportional variations
	of the number of sustain pulses in each subfield}
3/2948	••••• {by increasing the total sustaining time with respect to other times in the frame}
3/296	•••• Driving circuits for producing the waveforms applied to the driving
2/20/5	electrodes
3/2965 3/297	 {using inductors for energy recovery} using opposed discharge type panels
3/297	using surface discharge panels
3/2983	
5/2985	arrangements}
3/2986	••••• {with more than 3 electrodes involved in the operation}
3/299	using alternate lighting of surface-type panels
3/30	using electroluminescent panels
3/32	• • • semiconductive, e.g. using light-emitting
	diodes [LED]
3/3208	organic, e.g. using organic light-emitting diodes [OLED]
3/3216	••••• using a passive matrix
3/3225	••••• using an active matrix
3/3233	••••• with pixel circuitry controlling the
	current through the light-emitting element
3/3241	••••••••••••••••••••••••••••••••••••••
	emitting element being set using
	a data current provided by the
	data driver, e.g. by using a two-
2/225	transistor current mirror
3/325	••••••••••••••••••••••••••••••••••••••
	setting phase, e.g. by using a
	switch for connecting the driving
	transistor to the data driver
3/3258	••••• with pixel circuitry controlling the
	voltage across the light-emitting element
3/3266	Details of drivers for scan electrodes
3/3275	Details of drivers for data electrodes
3/3283	••••••• in which the data driver supplies a
	variable data current for setting the
	current through, or the voltage across,
3/3291	the light-emitting elements
3/3291	variable data voltage for setting the
	current through, or the voltage across,
	the light-emitting elements
3/34	• • by control of light from an independent source
3/3406	{Control of illumination source (illumination
	devices structurally associated with liquid crystal cells <u>G02F 1/1336</u>)}
3/3413	• • • {Details of control of colour illumination
2.0.110	sources}
3/342	• • • • {using several illumination sources
	separately controlled corresponding to
	different display panel areas, e.g. along one dimension such as lines}
	entrension such as intesy

3/3426	{the different display panel areas being distributed in two dimensions, e.g. matrix}
3/3433	• • {using light modulating elements actuated by an electric field and being other than liquid
	crystal devices and electrochromic devices (using liquid crystal devices G09G 3/36; using
	electrochromic devices <u>G09G 3/38</u> }
3/344	• • • • {based on particles moving in a fluid
	or in a gas, e.g. electrophoretic devices
	(electrophoretic devices <u>per se $G02F 1/167$</u>)}
3/3446	•••• {with more than two electrodes controlling the modulating element}
3/3453	• • • • {based on rotating particles or
	microelements }
3/346	• • • • {based on modulation of the reflection angle,
	e.g. micromirrors (micromirrors devices <u>per</u> se <u>G02B 26/0833</u>)}
3/3466	• • • {based on interferometric effect}
3/3473	• • • • {based on light coupled out of a light guide,
0,01,0	e.g. due to scattering, by contracting the light
	guide with external means}
3/348	• • • {based on the deformation of a fluid drop,
	e.g. electrowetting}
3/3486	• • • {using light modulating elements actuated by a
3/3493	magnetic field}fusing light modulating elements actuated by a
5/5495	piezoelectric effect}
3/36	using liquid crystals
3/3603	• • • { with thermally addressed liquid crystals }
3/3607	{ for displaying colours or for displaying
	grey scales with a specific pixel layout,
	e.g. using sub-pixels (display of colours in
	flat matrix panels other than liquid crystal displays G09G 3/2003; grey scales specific
	displays G09G 3/2003; grey scales specific
3/3611	
3/3611	 displays <u>G09G 3/2003</u>; grey scales specific for television <u>H04N 3/127</u>) {Control of matrices with row and column drivers}
3/3614	 displays <u>G09G 3/2003</u>; grey scales specific for television <u>H04N 3/127</u>) {Control of matrices with row and column drivers} {Control of polarity reversal in general}
	 displays <u>G09G 3/2003</u>; grey scales specific for television <u>H04N 3/127</u>) {Control of matrices with row and column drivers} {Control of polarity reversal in general} {with automatic refresh of the display
3/3614 3/3618	 displays <u>G09G 3/2003</u>; grey scales specific for television <u>H04N 3/127</u>) {Control of matrices with row and column drivers} {Control of polarity reversal in general} {with automatic refresh of the display panel using sense/write circuits}
3/3614	 displays <u>G09G 3/2003</u>; grey scales specific for television <u>H04N 3/127</u>) {Control of matrices with row and column drivers} {Control of polarity reversal in general} {with automatic refresh of the display panel using sense/write circuits} {using a passive matrix
3/3614 3/3618	 displays <u>G09G 3/2003</u>; grey scales specific for television <u>H04N 3/127</u>) {Control of matrices with row and column drivers} {Control of polarity reversal in general} {with automatic refresh of the display panel using sense/write circuits} {using a passive matrix (<u>G09G 3/3674</u> - <u>G09G 3/3696</u> take
3/3614 3/3618	 displays <u>G09G 3/2003</u>; grey scales specific for television <u>H04N 3/127</u>) {Control of matrices with row and column drivers} {Control of polarity reversal in general} {with automatic refresh of the display panel using sense/write circuits} {using a passive matrix
3/3614 3/3618 3/3622	 displays <u>G09G 3/2003</u>; grey scales specific for television <u>H04N 3/127</u>) {Control of matrices with row and column drivers} {Control of polarity reversal in general} {with automatic refresh of the display panel using sense/write circuits} {using a passive matrix (<u>G09G 3/3674 - G09G 3/3696</u> take precedence)} {using active addressing} {using liquid crystals having memory
3/3614 3/3618 3/3622 3/3625 3/3629	 displays <u>G09G 3/2003</u>; grey scales specific for television <u>H04N 3/127</u>) {Control of matrices with row and column drivers} {Control of polarity reversal in general} {with automatic refresh of the display panel using sense/write circuits} {using a passive matrix (<u>G09G 3/3674 - G09G 3/3696</u> take precedence)} {using active addressing} {using liquid crystals having memory effects, e.g. ferroelectric liquid crystals}
3/3614 3/3618 3/3622 3/3625	 displays <u>G09G 3/2003</u>; grey scales specific for television <u>H04N 3/127</u>) {Control of matrices with row and column drivers} {Control of polarity reversal in general} {with automatic refresh of the display panel using sense/write circuits} {using a passive matrix (G09G 3/3674 - G09G 3/3696 take precedence)} {using active addressing} {using liquid crystals having memory effects, e.g. ferroelectric liquid crystals} {with transmission/voltage
3/3614 3/3618 3/3622 3/3625 3/3629	 displays <u>G09G 3/2003</u>; grey scales specific for television <u>H04N 3/127</u>) {Control of matrices with row and column drivers} {Control of polarity reversal in general} {with automatic refresh of the display panel using sense/write circuits} {using a passive matrix (<u>G09G 3/3674 - G09G 3/3696</u> take precedence)} {using active addressing} {using liquid crystals having memory effects, e.g. ferroelectric liquid crystals} {with transmission/voltage characteristic comprising multiple
3/3614 3/3618 3/3622 3/3625 3/3629	 displays G09G 3/2003; grey scales specific for television H04N 3/127)} {Control of matrices with row and column drivers} {Control of polarity reversal in general} {with automatic refresh of the display panel using sense/write circuits} {using a passive matrix (G09G 3/3674 - G09G 3/3696 take precedence)} {using active addressing} {using liquid crystals having memory effects, e.g. ferroelectric liquid crystals} {with transmission/voltage characteristic comprising multiple loops, e.g. antiferroelectric liquid
3/3614 3/3618 3/3622 3/3625 3/3629 3/3633	 displays <u>G09G 3/2003</u>; grey scales specific for television <u>H04N 3/127</u>) {Control of matrices with row and column drivers} {Control of polarity reversal in general} {with automatic refresh of the display panel using sense/write circuits} {using a passive matrix (G09G 3/3674 - G09G 3/3696 take precedence)} {using active addressing} {using liquid crystals having memory effects, e.g. ferroelectric liquid crystals} {with transmission/voltage characteristic comprising multiple loops, e.g. antiferroelectric liquid crystals}
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3/3614 3/3618 3/3622 3/3625 3/3629 3/3633	 displays <u>G09G 3/2003</u>; grey scales specific for television <u>H04N 3/127</u>) {Control of matrices with row and column drivers} {Control of polarity reversal in general} {With automatic refresh of the display panel using sense/write circuits} {using a passive matrix (G09G 3/3674 - G09G 3/3696 take precedence)} {Using active addressing} {Using liquid crystals having memory effects, e.g. ferroelectric liquid crystals} {with transmission/voltage characteristic comprising multiple loops, e.g. antiferroelectric liquid crystals} {with intermediate tones displayed by domain size control (domain size control in flat matrix panels other than liquid crystal displays having memory
3/3614 3/3618 3/3622 3/3625 3/3629 3/3633 3/3633	 displays <u>G09G 3/2003</u>; grey scales specific for television <u>H04N 3/127</u>) {Control of matrices with row and column drivers} {Control of polarity reversal in general} {With automatic refresh of the display panel using sense/write circuits} {using a passive matrix (<u>G09G 3/3674</u> - <u>G09G 3/3696</u> take precedence)} {using active addressing} {using liquid crystals having memory effects, e.g. ferroelectric liquid crystals} {with transmission/voltage characteristic comprising multiple loops, e.g. antiferroelectric liquid crystals} {with intermediate tones displayed by domain size control (domain size control in flat matrix panels other than liquid crystal displays having memory effects <u>G09G 3/207</u>)
3/3614 3/3618 3/3622 3/3625 3/3629 3/3633 3/3637 3/364	 displays <u>G09G 3/2003</u>; grey scales specific for television <u>H04N 3/127</u>) {Control of matrices with row and column drivers} {Control of polarity reversal in general} {with automatic refresh of the display panel using sense/write circuits} {using a passive matrix (<u>G09G 3/3674 - G09G 3/3696</u> take precedence)} {using active addressing} {using liquid crystals having memory effects, e.g. ferroelectric liquid crystals} {with transmission/voltage characteristic comprising multiple loops, e.g. antiferroelectric liquid crystals} {with intermediate tones displayed by domain size control (domain size control in flat matrix panels other than liquid crystal displays having memory effects <u>G09G 3/207</u>) {with use of subpixels}
3/3614 3/3618 3/3622 3/3625 3/3629 3/3633 3/3633 3/3637	 displays <u>G09G 3/2003</u>; grey scales specific for television <u>H04N 3/127</u>) {Control of matrices with row and column drivers} {Control of polarity reversal in general} {With automatic refresh of the display panel using sense/write circuits} {using a passive matrix (<u>G09G 3/3674 - G09G 3/3696</u> take precedence)} {using active addressing} {using liquid crystals having memory effects, e.g. ferroelectric liquid crystals} {with transmission/voltage characteristic comprising multiple loops, e.g. antiferroelectric liquid crystals} {with intermediate tones displayed by domain size control (domain size control in flat matrix panels other than liquid crystal displays having memory effects <u>G09G 3/207</u>) {with the matrix divided into sections}
3/3614 3/3618 3/3622 3/3625 3/3629 3/3633 3/3637 3/364	 displays <u>G09G 3/2003</u>; grey scales specific for television <u>H04N 3/127</u>) {Control of matrices with row and column drivers} {Control of polarity reversal in general} {with automatic refresh of the display panel using sense/write circuits} {using a passive matrix (<u>G09G 3/3674 - G09G 3/3696</u> take precedence)} {using active addressing} {using liquid crystals having memory effects, e.g. ferroelectric liquid crystals} {with transmission/voltage characteristic comprising multiple loops, e.g. antiferroelectric liquid crystals} {with intermediate tones displayed by domain size control (domain size control in flat matrix panels other than liquid crystal displays having memory effects <u>G09G 3/207</u>) {with the matrix divided into sections} {with the matrix divided into sections}
3/3614 3/3618 3/3622 3/3625 3/3629 3/3633 3/3633 3/3637	 displays <u>G09G 3/2003</u>; grey scales specific for television <u>H04N 3/127</u>) {Control of matrices with row and column drivers} {Control of polarity reversal in general} {With automatic refresh of the display panel using sense/write circuits} {using a passive matrix (<u>G09G 3/3674 - G09G 3/3696</u> take precedence)} {using active addressing} {using liquid crystals having memory effects, e.g. ferroelectric liquid crystals} {with transmission/voltage characteristic comprising multiple loops, e.g. antiferroelectric liquid crystals} {with intermediate tones displayed by domain size control (domain size control in flat matrix panels other than liquid crystal displays having memory effects <u>G09G 3/207</u>) {with the matrix divided into sections} {with the matrix divided into sections} {using an active matrix (<u>G09G 3/367 - G09G 3/3696</u> take
3/3614 3/3618 3/3622 3/3625 3/3629 3/3633 3/3633 3/3637	 displays <u>G09G 3/2003</u>; grey scales specific for television <u>H04N 3/127</u>) {Control of matrices with row and column drivers} {Control of polarity reversal in general} {with automatic refresh of the display panel using sense/write circuits} {using a passive matrix (<u>G09G 3/3674 - G09G 3/3696</u> take precedence)} {using active addressing} {using liquid crystals having memory effects, e.g. ferroelectric liquid crystals} {with transmission/voltage characteristic comprising multiple loops, e.g. antiferroelectric liquid crystals} {with intermediate tones displayed by domain size control (domain size control in flat matrix panels other than liquid crystal displays having memory effects <u>G09G 3/207</u>) {with the matrix divided into sections} {with the matrix divided into sections}
3/3614 3/3618 3/3622 3/3625 3/3629 3/3633 3/3633 3/3637 3/364 3/3644 3/3644	 displays G09G 3/2003; grey scales specific for television H04N 3/127) {Control of matrices with row and column drivers} {Control of polarity reversal in general} {With automatic refresh of the display panel using sense/write circuits} {using a passive matrix (G09G 3/3674 - G09G 3/3696 take precedence)} {using active addressing} {using liquid crystals having memory effects, e.g. ferroelectric liquid crystals} {with transmission/voltage characteristic comprising multiple loops, e.g. antiferroelectric liquid crystals} {with intermediate tones displayed by domain size control (domain size control in flat matrix panels other than liquid crystal displays having memory effects <u>G09G 3/207</u>) {with the matrix divided into sections} {with the matrix divided into sections} {using an active matrix (G09G 3/367 - G09G 3/3696 take precedence)}

3/3655	••••• {Details of drivers for counter
	electrodes, e.g. common electrodes
	for pixel capacitors or supplementary
	storage capacitors }
3/3659	••••• {the addressing of the pixel involving
5/5057	the control of two or more scan
	electrodes or two or more data
	electrodes of two of more data electrodes, e.g. pixel voltage dependant
	on signal of two data electrodes}
2/2662	-
3/3662	{using plasma-addressed liquid crystal
	displays}
3/3666	••••• {with the matrix divided into sections}
3/367	•••• {with a nonlinear element in series with
	the liquid crystal cell, e.g. a diode, or
	M.I.M. element}
3/3674	• • • • {Details of drivers for scan electrodes}
3/3677	••••• {suitable for active matrices only}
3/3681	••••• {suitable for passive matrices only}
3/3685	• • • • {Details of drivers for data electrodes}
3/3688	• • • • • {suitable for active matrices only}
3/3692	
3/3696	• • • • {Generation of voltages supplied to
2 /2 0	electrode drivers}
3/38	using electrochromic devices
5/00	Control arrangements or circuits for visual
	indicators common to cathode-ray tube indicators
	and other visual indicators (image data processing
	or generation, in general <u>G06T</u>)
5/001	• {Arbitration of resources in a display system, e.g.
	control of access to frame buffer by video controller
	1/
	and/or main processor {
5/003	and/or main processor} {Details of a display terminal, the details relating
5/003	• {Details of a display terminal, the details relating
5/003	• {Details of a display terminal, the details relating to the control arrangement of the display terminal
5/003	• {Details of a display terminal, the details relating to the control arrangement of the display terminal and to the interfaces thereto (specific for a CRT
	• {Details of a display terminal, the details relating to the control arrangement of the display terminal and to the interfaces thereto (specific for a CRT <u>G09G 1/165</u> ; for a flat panel <u>G09G 3/2092</u>)}
5/003 5/005	 {Details of a display terminal, the details relating to the control arrangement of the display terminal and to the interfaces thereto (specific for a CRT <u>G09G 1/165</u>; for a flat panel <u>G09G 3/2092</u>)} {Adapting incoming signals to the display format
5/005	 {Details of a display terminal, the details relating to the control arrangement of the display terminal and to the interfaces thereto (specific for a CRT <u>G09G 1/165</u>; for a flat panel <u>G09G 3/2092</u>)} {Adapting incoming signals to the display format of the display terminal}
	 {Details of a display terminal, the details relating to the control arrangement of the display terminal and to the interfaces thereto (specific for a CRT <u>G09G 1/165</u>; for a flat panel <u>G09G 3/2092</u>)} {Adapting incoming signals to the display format of the display terminal} {Details of the interface to the display terminal
5/005	 {Details of a display terminal, the details relating to the control arrangement of the display terminal and to the interfaces thereto (specific for a CRT <u>G09G 1/165;</u> for a flat panel <u>G09G 3/2092</u>)} {Adapting incoming signals to the display format of the display terminal} {Details of the interface to the display terminal (specific for a display terminal using a CRT
5/005	 {Details of a display terminal, the details relating to the control arrangement of the display terminal and to the interfaces thereto (specific for a CRT <u>G09G 1/165</u>; for a flat panel <u>G09G 3/2092</u>)} {Adapting incoming signals to the display format of the display terminal} {Details of the interface to the display terminal (specific for a display terminal using a CRT <u>G09G 1/167</u>; using a flat panel <u>G09G 3/2096</u>;
5/005	 {Details of a display terminal, the details relating to the control arrangement of the display terminal and to the interfaces thereto (specific for a CRT <u>G09G 1/165</u>; for a flat panel <u>G09G 3/2092</u>)} {Adapting incoming signals to the display format of the display terminal} {Details of the interface to the display terminal (specific for a display terminal using a CRT <u>G09G 1/167</u>; using a flat panel <u>G09G 3/2096</u>; circuits for interfacing with colour displays
5/005 5/006	 {Details of a display terminal, the details relating to the control arrangement of the display terminal and to the interfaces thereto (specific for a CRT <u>G09G 1/165;</u> for a flat panel <u>G09G 3/2092</u>)} {Adapting incoming signals to the display format of the display terminal} {Details of the interface to the display terminal (specific for a display terminal using a CRT <u>G09G 1/167;</u> using a flat panel <u>G09G 3/2096;</u> circuits for interfacing with colour displays <u>G09G 5/04</u>)}
5/005 5/006 5/008	 {Details of a display terminal, the details relating to the control arrangement of the display terminal and to the interfaces thereto (specific for a CRT <u>G09G 1/165;</u> for a flat panel <u>G09G 3/2092</u>)} {Adapting incoming signals to the display format of the display terminal} {Details of the interface to the display terminal (specific for a display terminal using a CRT <u>G09G 1/167;</u> using a flat panel <u>G09G 3/2096;</u> circuits for interfacing with colour displays <u>G09G 5/04</u>)} {Clock recovery}
5/005 5/006	 {Details of a display terminal, the details relating to the control arrangement of the display terminal and to the interfaces thereto (specific for a CRT <u>G09G 1/165;</u> for a flat panel <u>G09G 3/2092</u>)} {Adapting incoming signals to the display format of the display terminal} {Details of the interface to the display terminal (specific for a display terminal using a CRT <u>G09G 1/167;</u> using a flat panel <u>G09G 3/2096;</u> circuits for interfacing with colour displays <u>G09G 5/04</u>)} {Clock recovery} characterised by the way in which colour is
5/005 5/006 5/008	 {Details of a display terminal, the details relating to the control arrangement of the display terminal and to the interfaces thereto (specific for a CRT <u>G09G 1/165;</u> for a flat panel <u>G09G 3/2092</u>)} {Adapting incoming signals to the display format of the display terminal} {Details of the interface to the display terminal (specific for a display terminal using a CRT <u>G09G 1/167;</u> using a flat panel <u>G09G 3/2096;</u> circuits for interfacing with colour displays <u>G09G 5/04</u>)} {Clock recovery} characterised by the way in which colour is displayed {(details of colour display specific for
5/005 5/006 5/008	 {Details of a display terminal, the details relating to the control arrangement of the display terminal and to the interfaces thereto (specific for a CRT <u>G09G 1/165;</u> for a flat panel <u>G09G 3/2092</u>)} {Adapting incoming signals to the display format of the display terminal} {Details of the interface to the display terminal (specific for a display terminal using a CRT <u>G09G 1/167;</u> using a flat panel <u>G09G 3/2096;</u> circuits for interfacing with colour displays <u>G09G 5/04</u>)} {Clock recovery} characterised by the way in which colour is displayed {(details of colour display specific for CRTs <u>G09G 1/28;</u> specific for flat matrix panels
5/005 5/006 5/008	 {Details of a display terminal, the details relating to the control arrangement of the display terminal and to the interfaces thereto (specific for a CRT <u>G09G 1/165</u>; for a flat panel <u>G09G 3/2092</u>)} {Adapting incoming signals to the display format of the display terminal} {Details of the interface to the display terminal (specific for a display terminal using a CRT <u>G09G 1/167</u>; using a flat panel <u>G09G 3/2096</u>; circuits for interfacing with colour displays <u>G09G 5/04</u>)} {Clock recovery} characterised by the way in which colour is displayed {(details of colour display specific for CRTs <u>G09G 1/28</u>; specific for flat matrix panels other than liquid crystal displays <u>G09G 3/2003</u>;
5/005 5/006 5/008 5/02	 {Details of a display terminal, the details relating to the control arrangement of the display terminal and to the interfaces thereto (specific for a CRT <u>G09G 1/165;</u> for a flat panel <u>G09G 3/2092</u>)} {Adapting incoming signals to the display format of the display terminal} {Details of the interface to the display terminal (specific for a display terminal using a CRT <u>G09G 1/167;</u> using a flat panel <u>G09G 3/2096;</u> circuits for interfacing with colour displays <u>G09G 5/04</u>)} {Clock recovery} characterised by the way in which colour is displayed {(details of colour display specific for CRTs <u>G09G 1/28;</u> specific for flat matrix panels other than liquid crystal displays <u>G09G 3/2003;</u> specific for liquid crystal displays <u>G09G 3/2003;</u>
5/005 5/006 5/008 5/02	 {Details of a display terminal, the details relating to the control arrangement of the display terminal and to the interfaces thereto (specific for a CRT <u>G09G 1/165;</u> for a flat panel <u>G09G 3/2092</u>)} {Adapting incoming signals to the display format of the display terminal} {Details of the interface to the display terminal (specific for a display terminal using a CRT <u>G09G 1/167;</u> using a flat panel <u>G09G 3/2096;</u> circuits for interfacing with colour displays <u>G09G 5/04</u>)} {Clock recovery} characterised by the way in which colour is displayed {(details of colour display specific for CRTs <u>G09G 1/28;</u> specific for flat matrix panels other than liquid crystal displays <u>G09G 3/2003;</u> specific for liquid crystal displays <u>G09G 3/2003;</u> {using memory planes}
5/005 5/006 5/008 5/02	 {Details of a display terminal, the details relating to the control arrangement of the display terminal and to the interfaces thereto (specific for a CRT <u>G09G 1/165;</u> for a flat panel <u>G09G 3/2092</u>)} {Adapting incoming signals to the display format of the display terminal} {Details of the interface to the display terminal (specific for a display terminal using a CRT <u>G09G 1/167;</u> using a flat panel <u>G09G 3/2096;</u> circuits for interfacing with colour displays <u>G09G 5/04</u>)} {Clock recovery} characterised by the way in which colour is displayed {(details of colour display specific for CRTs <u>G09G 1/28;</u> specific for flat matrix panels other than liquid crystal displays <u>G09G 3/2003;</u> specific for liquid crystal displays <u>G09G 3/3607</u>)} {using memory planes} {using colour registers, e.g. to control
5/005 5/006 5/008 5/02	 {Details of a display terminal, the details relating to the control arrangement of the display terminal and to the interfaces thereto (specific for a CRT <u>G09G 1/165;</u> for a flat panel <u>G09G 3/2092</u>)} {Adapting incoming signals to the display format of the display terminal} {Details of the interface to the display terminal (specific for a display terminal using a CRT <u>G09G 1/167;</u> using a flat panel <u>G09G 3/2096;</u> circuits for interfacing with colour displays <u>G09G 5/04</u>)} {Clock recovery} characterised by the way in which colour is displayed {(details of colour display specific for CRTs <u>G09G 1/28;</u> specific for flat matrix panels other than liquid crystal displays <u>G09G 3/2003;</u> specific for liquid crystal displays <u>G09G 3/3607</u>)} {using memory planes} {using colour registers, e.g. to control background, foreground, surface filling
5/005 5/006 5/008 5/02 5/022 5/022	 {Details of a display terminal, the details relating to the control arrangement of the display terminal and to the interfaces thereto (specific for a CRT G09G 1/165; for a flat panel G09G 3/2092)} {Adapting incoming signals to the display format of the display terminal} {Details of the interface to the display terminal (specific for a display terminal using a CRT G09G 1/167; using a flat panel G09G 3/2096; circuits for interfacing with colour displays G09G 5/04)} {Clock recovery} characterised by the way in which colour is displayed {(details of colour display specific for CRTs G09G 1/28; specific for flat matrix panels other than liquid crystal displays G09G 3/2003; specific for liquid crystal displays G09G 3/2003; {using memory planes} {using colour registers, e.g. to control background, foreground, surface filling (G09G 5/06 takes precedence)}
5/005 5/006 5/008 5/02	 {Details of a display terminal, the details relating to the control arrangement of the display terminal and to the interfaces thereto (specific for a CRT G09G 1/165; for a flat panel G09G 3/2092)} {Adapting incoming signals to the display format of the display terminal} {Details of the interface to the display terminal (specific for a display terminal using a CRT G09G 1/167; using a flat panel G09G 3/2096; circuits for interfacing with colour displays G09G 5/04)} {Clock recovery} characterised by the way in which colour is displayed {(details of colour display specific for CRTs G09G 1/28; specific for flat matrix panels other than liquid crystal displays G09G 3/2003; specific for liquid crystal displays G09G 3/3607)} {using memory planes} {using colour registers, e.g. to control background, foreground, surface filling (G09G 5/06 takes precedence)} {Control of mixing and/or overlay of colours
5/005 5/006 5/008 5/02 5/022 5/022	 {Details of a display terminal, the details relating to the control arrangement of the display terminal and to the interfaces thereto (specific for a CRT G09G 1/165; for a flat panel G09G 3/2092)} {Adapting incoming signals to the display format of the display terminal} {Details of the interface to the display terminal (specific for a display terminal using a CRT G09G 1/167; using a flat panel G09G 3/2096; circuits for interfacing with colour displays G09G 5/04)} {Clock recovery} characterised by the way in which colour is displayed {(details of colour display specific for CRTs G09G 1/28; specific for flat matrix panels other than liquid crystal displays G09G 3/2003; specific for liquid crystal displays G09G 3/2003; specific for liquid crystal displays G09G 3/3607)} {using memory planes} {using colour registers, e.g. to control background, foreground, surface filling (G09G 5/06 takes precedence)} {Control of mixing and/or overlay of colours in general (G09G 5/022 and G09G 5/024 take
5/005 5/006 5/008 5/02 5/022 5/022 5/024	 {Details of a display terminal, the details relating to the control arrangement of the display terminal and to the interfaces thereto (specific for a CRT <u>G09G 1/165;</u> for a flat panel <u>G09G 3/2092</u>)} {Adapting incoming signals to the display format of the display terminal} {Details of the interface to the display terminal (specific for a display terminal using a CRT <u>G09G 1/167;</u> using a flat panel <u>G09G 3/2096;</u> circuits for interfacing with colour displays <u>G09G 5/04</u>)} {Clock recovery} characterised by the way in which colour is displayed {(details of colour display specific for CRTs <u>G09G 1/28;</u> specific for flat matrix panels other than liquid crystal displays <u>G09G 3/2003;</u> specific for liquid crystal displays <u>G09G 3/3607</u>)} {using memory planes} {using colour registers, e.g. to control background, foreground, surface filling (<u>G09G 5/06</u> takes precedence)} {Control of mixing and/or overlay of colours in general (<u>G09G 5/022</u> and <u>G09G 5/024</u> take precedence)}
5/005 5/006 5/008 5/02 5/022 5/022	 {Details of a display terminal, the details relating to the control arrangement of the display terminal and to the interfaces thereto (specific for a CRT G09G 1/165; for a flat panel G09G 3/2092)} {Adapting incoming signals to the display format of the display terminal} {Details of the interface to the display terminal (specific for a display terminal using a CRT G09G 1/167; using a flat panel G09G 3/2096; circuits for interfacing with colour displays G09G 5/04)} {Clock recovery} characterised by the way in which colour is displayed {(details of colour display specific for CRTs G09G 1/28; specific for flat matrix panels other than liquid crystal displays G09G 3/2003; specific for liquid crystal displays G09G 3/3607)} {using memory planes} {using colour registers, e.g. to control background, foreground, surface filling (G09G 5/06 takes precedence)} {Control of mixing and/or overlay of colours in general (G09G 5/022 and G09G 5/024 take precedence)} {Circuits for converting colour display signals
5/005 5/006 5/008 5/02 5/022 5/022 5/024 5/026 5/028	 {Details of a display terminal, the details relating to the control arrangement of the display terminal and to the interfaces thereto (specific for a CRT G09G 1/165; for a flat panel G09G 3/2092)} {Adapting incoming signals to the display format of the display terminal} {Details of the interface to the display terminal (specific for a display terminal using a CRT G09G 1/167; using a flat panel G09G 3/2096; circuits for interfacing with colour displays G09G 5/04)} {Clock recovery} characterised by the way in which colour is displayed {(details of colour display specific for CRTs G09G 1/28; specific for flat matrix panels other than liquid crystal displays G09G 3/2003; specific for liquid crystal displays G09G 3/3607)} {using memory planes} {using colour registers, e.g. to control background, foreground, surface filling (G09G 5/06 takes precedence)} {Control of mixing and/or overlay of colours in general (G09G 5/022 and G09G 5/024 take precedence)} {Circuits for converting colour display signals into monochrome display signals}
5/005 5/006 5/008 5/02 5/022 5/024 5/026 5/028 5/028 5/04	 {Details of a display terminal, the details relating to the control arrangement of the display terminal and to the interfaces thereto (specific for a CRT G09G 1/165; for a flat panel G09G 3/2092)} {Adapting incoming signals to the display format of the display terminal} {Details of the interface to the display terminal (specific for a display terminal using a CRT G09G 1/167; using a flat panel G09G 3/2096; circuits for interfacing with colour displays G09G 5/04)} {Clock recovery} characterised by the way in which colour is displayed {(details of colour display specific for CRTs G09G 1/28; specific for flat matrix panels other than liquid crystal displays G09G 3/2003; specific for liquid crystal displays G09G 3/3607)} {using memory planes} {using colour registers, e.g. to control background, foreground, surface filling (G09G 5/06 takes precedence)} {Circuits for converting colour display signals into monochrome display signals} using circuits for interfacing with colour display signals
5/005 5/006 5/008 5/02 5/022 5/024 5/026 5/028 5/04 5/06	 {Details of a display terminal, the details relating to the control arrangement of the display terminal and to the interfaces thereto (specific for a CRT G09G 1/165; for a flat panel G09G 3/2092)} {Adapting incoming signals to the display format of the display terminal} {Details of the interface to the display terminal (specific for a display terminal using a CRT G09G 1/167; using a flat panel G09G 3/2096; circuits for interfacing with colour displays G09G 5/04)} {Clock recovery} characterised by the way in which colour is displayed {(details of colour display specific for CRTs G09G 1/28; specific for flat matrix panels other than liquid crystal displays G09G 3/2003; specific for liquid crystal displays G09G 3/2003; specific for liquid crystal displays G09G 3/2003; specific for liquid crystal displays G09G 3/3607)} {using memory planes} {control of mixing and/or overlay of colours in general (G09G 5/022 and G09G 5/024 take precedence)} {Circuits for converting colour display signals into monochrome display signals} using circuits for interfacing with colour displays
5/005 5/006 5/008 5/02 5/022 5/024 5/026 5/028 5/028 5/04 5/06 5/08	 {Details of a display terminal, the details relating to the control arrangement of the display terminal and to the interfaces thereto (specific for a CRT G09G 1/165; for a flat panel G09G 3/2092)} {Adapting incoming signals to the display format of the display terminal} {Details of the interface to the display terminal (specific for a display terminal using a CRT G09G 1/167; using a flat panel G09G 3/2096; circuits for interfacing with colour displays G09G 5/04)} {Clock recovery} characterised by the way in which colour is displayed {(details of colour display specific for CRTs G09G 1/28; specific for flat matrix panels other than liquid crystal displays G09G 3/2003; specific for liquid crystal displays G09G 3/3607)} {using memory planes} {using colour registers, e.g. to control background, foreground, surface filling (G09G 5/06 takes precedence)} {Control of mixing and/or overlay of colours in general (G09G 5/022 and G09G 5/024 take precedence)} using circuits for interfacing with colour displays signals into monochrome display signals} using circuits for interfacing with colour displays
5/005 5/006 5/008 5/02 5/022 5/024 5/026 5/028 5/028 5/04 5/06 5/08 5/10	 {Details of a display terminal, the details relating to the control arrangement of the display terminal and to the interfaces thereto (specific for a CRT G09G 1/165; for a flat panel G09G 3/2092)} {Adapting incoming signals to the display format of the display terminal {Details of the interface to the display terminal (specific for a display terminal using a CRT G09G 1/167; using a flat panel G09G 3/2096; circuits for interfacing with colour displays G09G 5/04)} {Clock recovery} characterised by the way in which colour is displayed {(details of colour display specific for CRTs G09G 1/28; specific for flat matrix panels other than liquid crystal displays G09G 3/2003; specific for liquid crystal displays G09G 3/2003; specific for liquid crystal displays G09G 3/3607)} {using memory planes} {using colour registers, e.g. to control background, foreground, surface filling (G09G 5/06 takes precedence)} {Control of mixing and/or overlay of colours in general (G09G 5/022 and G09G 5/024 take precedence)} using circuits for interfacing with colour displays into monochrome display signals using colour palettes, e.g. look-up tables Cursor circuits Intensity circuits
5/005 5/006 5/008 5/02 5/022 5/024 5/026 5/028 5/028 5/04 5/06 5/08	 {Details of a display terminal, the details relating to the control arrangement of the display terminal and to the interfaces thereto (specific for a CRT G09G 1/165; for a flat panel G09G 3/2092)} {Adapting incoming signals to the display format of the display terminal} {Details of the interface to the display terminal (specific for a display terminal using a CRT G09G 1/167; using a flat panel G09G 3/2096; circuits for interfacing with colour displays G09G 5/04)} {Clock recovery} characterised by the way in which colour is displayed {(details of colour display specific for CRTs G09G 1/28; specific for flat matrix panels other than liquid crystal displays G09G 3/2003; specific for liquid crystal displays G09G 3/2003; specific for liquid crystal displays G09G 3/3607)} {using memory planes} {using colour registers, e.g. to control background, foreground, surface filling (G09G 5/06 takes precedence)} {Control of mixing and/or overlay of colours in general (G09G 5/022 and G09G 5/024 take precedence)} {Circuits for converting colour display signals into monochrome display signals} using circuits for interfacing with colour displays Synchronisation between the display unit and other
5/005 5/006 5/008 5/02 5/022 5/024 5/026 5/028 5/028 5/04 5/06 5/08 5/10	 {Details of a display terminal, the details relating to the control arrangement of the display terminal and to the interfaces thereto (specific for a CRT G09G 1/165; for a flat panel G09G 3/2092)} {Adapting incoming signals to the display format of the display terminal} {Details of the interface to the display terminal (specific for a display terminal using a CRT G09G 1/167; using a flat panel G09G 3/2096; circuits for interfacing with colour displays G09G 5/04)} {Clock recovery} characterised by the way in which colour is displayed {(details of colour display specific for CRTs G09G 1/28; specific for flat matrix panels other than liquid crystal displays G09G 3/2003; specific for liquid crystal displays G09G 3/2002; specific for liquid crystal displays G09G 3/2003; specific for liquid crystal displays G09G 3/2002; specific for liquid crystal displays G09G 3/2003; specific for liquid crystal displays G09G 3/2004; {using colour registers, e.g. to control background, foreground, surface filling (G09G 5/06 takes precedence)} {Curtcuits for converting colour display signals into monochrome display signals} using circuits for interfacing with colour displays using circuits for interfacing with colour displays using circuits for interfacing with colour displays using colour palettes, e.g. look-up tables Cursor circuits
5/005 5/006 5/008 5/02 5/022 5/024 5/026 5/028 5/028 5/04 5/06 5/08 5/10	 {Details of a display terminal, the details relating to the control arrangement of the display terminal and to the interfaces thereto (specific for a CRT G09G 1/165; for a flat panel G09G 3/2092)} {Adapting incoming signals to the display format of the display terminal} {Details of the interface to the display terminal (specific for a display terminal using a CRT G09G 1/167; using a flat panel G09G 3/2096; circuits for interfacing with colour displays G09G 5/04)} {Clock recovery} characterised by the way in which colour is displayed {(details of colour display specific for CRTs G09G 1/28; specific for flat matrix panels other than liquid crystal displays G09G 3/2003; specific for liquid crystal displays G09G 3/2003; specific for liquid crystal displays G09G 3/3607)} {using memory planes} {using colour registers, e.g. to control background, foreground, surface filling (G09G 5/06 takes precedence)} {Control of mixing and/or overlay of colours in general (G09G 5/022 and G09G 5/024 take precedence)} {Circuits for converting colour display signals into monochrome display signals} using circuits for interfacing with colour displays Synchronisation between the display unit and other

5/16	• Display of right-to-left language
5/18	• Timing circuits for raster scan displays (specially adapted for television <u>H04N</u> {; synchronisation between the display unit and other display units,
5/20	 videodisc player <u>G09G 5/12</u>) Function-generator circuits, e.g. circle generators {line or curve smoothing circuits}
5/22	 characterised by the display of characters or indicia using display control signals derived from coded signals representing the characters or indicia, e.g. with a character-code memory
5/222	• • {Control of the character-code memory}
5/225	 . {comprising a loadable character generator (character generators per se <u>G09G 5/24</u>)}
5/227	• • • {Resolution modifying circuits, e.g. variable screen formats, resolution change between
	memory contents and display screen}
5/24	• Generation of individual character patterns
5/243	• • • {Circuits for displaying proportional spaced
	characters or for kerning}
5/246	• • • {of ideographic or arabic-like characters}
5/26	for modifying the character dimensions, e.g. double width, double height
5/28	for enhancement of character form, e.g. smoothing
5/30	• • Control of display attribute
5/32	 with means for controlling the display position {(see provisionally <u>G09G 5/42</u>)}
5/34	• for rolling or scrolling
5/343	• { for systems having a character code-mapped display memory }
5/346	 • {for systems having a bit-mapped display memory}
5/26	
5/36	• characterised by the display of a graphic pattern,
	e.g. using an all-points-addressable [APA] memory
5/363	• • {Graphics controllers}
5/366	• • • {with conversion of CRT control signals to flat
	panel control signals, e.g. adapting the palette memory}
5/37	• Details of the operation on graphic patterns (<u>G09G 5/38</u> takes precedence)
5/373	• • for modifying the size of the graphic pattern
5/373 5/377	 for mourying the size of the graphic pattern for mixing or overlaying two or more graphic
5/5/1	patterns (<u>G09G 5/02</u> , <u>G09G 5/397</u> take precedence)
5/38	• • with means for controlling the display position
5/39	Control of the bit-mapped memory
5/391	• • Resolution modifying circuits, e.g. variable screen formats
5/393	• • Arrangements for updating the contents of the bit-mapped memory
5/395	• • Arrangements specially adapted for transferring the contents of the bit-mapped memory to the
	screen (G09G 5/399 takes precedence)
5/397	Arrangements specially adapted for
	transferring the contents of two or more
	bit-mapped memories to the screen
	simultaneously, e.g. for mixing or overlay
	(G09G 5/02 takes precedence)
	WARNING
	Not complete. See also G09G 5/395,
	<u>G09G 5/399</u>

5/399	• • using two or more bit-mapped memories, the operations of which are switched in time, e.g. ping-pong buffers
5/40	• characterised by the way in which both a pattern determined by character code and another pattern
	are displayed simultaneously, or either pattern is displayed selectively, e.g. with character code memory and APA, i.e. all-points-addressable, memory
5/42	 characterised by the display of patterns using a display memory without fixed position
	correspondence between the display memory contents and the display position on the screen
2230/00	Details of flat display driving waveforms
2290/00	Indexing scheme relating to details of a display
	terminal
2300/00	Aspects of the constitution of display devices
2300/00	Composition of display devices
2300/02	Display panel composed of stacked panels
2300/025	 Display panel composed of stacked panels Video wall, i.e. juxtaposition of a plurality of
2000,020	screens to create a display screen of bigger dimensions
2300/04	• Structural and physical details of display devices
2300/0404	Matrix technologies
2300/0404	••• Integration of the drivers onto the display
2500/0100	substrate
2300/0413	Details of dummy pixels or dummy lines in flat panels
2300/0417	Special arrangements specific to the use of low carrier mobility technology
2300/0421	Structural details of the set of electrodes
2300/0426	Layout of electrodes and connections
2300/043	Compensation electrodes or other additional
	electrodes in matrix displays related to
	distortions or compensation signals, e.g. for modifying TFT threshold voltage in column driver
2300/0434	• • Flat panel display in which a field is applied parallel to the display plane
2300/0439	• • Pixel structures
2300/0443	• • • with several sub-pixels for the same colour
	in a pixel, not specifically used to display gradations (G09G 3/364 takes precedence)
2300/0447	for multi-domain technique to improve the
	viewing angle in a liquid crystal display, such as multi-vertical alignment [MVA]
2300/0452	• • • Details of colour pixel setup, e.g. pixel
	composed of a red, a blue and two green components
2300/0456	• • • with a reflective area and a transmissive area
	combined in one pixel, such as in transflectance pixels
2300/046	• • • with an emissive area and a light-modulating area combined in one pixel
2300/0465	Improved aperture ratio, e.g. by size reduction
	of the pixel circuit, e.g. for improving the pixel
	density or the maximum displayable luminance or brightness
2300/0469	• • Details of the physics of pixel operation
2300/0473	• • Use of light emitting or modulating elements
	having two or more stable states when no
	power is applied

2300/0478	• • • related to liquid crystal pixels
2300/0482	Use of memory effects in nematic liquid
2200/0496	crystals
2300/0486	Cholesteric liquid crystals, including chiral-nematic liquid crystals, with
	transitions between focal conic, planar,
	and homeotropic states
2300/0491	• • • Use of a bi-refringent liquid crystal, optically
2300/0491	controlled bi-refringence [OCB] with bend
	and splay states, or electrically controlled bi-
	refringence [ECB] for controlling the color
2300/0495	• • • Use of transitions between isotropic and
2300/04/3	anisotropic phases in liquid crystals, by
	voltage controlled deformation of the liquid
	crystal molecules, as opposed to merely
	changing the orientation of the molecules
	as in, e.g. twisted-nematic [TN], vertical-
	aligned [VA], cholesteric, in-plane, or bi-
	refringent liquid crystals
2300/06	• Passive matrix structure, i.e. with direct application
	of both column and row voltages to the light
	emitting or modulating elements, other than LCD or
	OLED
2300/08	• Active matrix structure, i.e. with use of active
	elements, inclusive of non-linear two terminal
	elements, in the pixels together with light emitting
2200/0004	or modulating elements
2300/0804	• Sub-multiplexed active matrix panel, i.e. wherein
	one active driving circuit is used at pixel level for multiple image producing elements
2300/0809	Several active elements per pixel in active matrix
2300/0809	panels
2300/0814	• • • used for selection purposes, e.g. logical AND
2300/0814	for partial update
2300/0819	• • • used for counteracting undesired variations,
2300,0017	e.g. feedback or autozeroing
2300/0823	• • • used to establish symmetry in driving, e.g. with
	polarity inversion
2300/0828	• • • forming a digital to analog [D/A] conversion
	circuit
2300/0833	• • • forming a linear amplifier or follower
2300/0838	• • • with level shifting
2300/0842	• • forming a memory circuit, e.g. a dynamic
	memory with one capacitor
2300/0847	• • • being a dynamic memory without any
	storage capacitor, i.e. with use of parasitic
	capacitances as storage elements
2300/0852	• • • being a dynamic memory with more than one
2200/00555	capacitor
2300/0857	• • • Static memory circuit, e.g. flip-flop
2300/0861	• • • • with additional control of the display period
	without amending the charge stored in a
	pixel memory, e.g. by means of additional select electrodes
2300/0866	• • • • by means of changes in the pixel supply
2300/0000	voltage
2300/0871	• • • with level shifting
2300/0871	 Supplementary capacities in pixels having special
2300,0070	driving circuits and electrodes instead of being
	connected to common electrode or ground; Use
	of additional capacitively coupled compensation
	electrodes
2300/088	• • using a non-linear two-terminal element

2300/0885	• • Pixel comprising a non-linear two-terminal element alone in series with each display pixel
2300/089	element Pixel comprising a non-linear two-terminal
	element in series with each display pixel element, the series comprising also other elements
2300/0895	• • having more than one selection line for a two- terminal active matrix LCD, e.g. Lechner and D2R circuits
2310/00	Command of the display device
2310/02	• Addressing, scanning or driving the display screen
	or processing steps related thereto
2310/0202	• • Addressing of scan or signal lines
2310/0205	Simultaneous scanning of several lines in flat panels
2310/0208	• • • using active addressing
2310/021	••• Double addressing, i.e. scanning two or more lines, e.g. lines 2 and 3; 4 and 5, at a time in a first field, followed by scanning two or more lines in another combination, e.g. lines 1 and 2; 3 and 4, in a second field
2310/0213	• • controlling the sequence of the scanning lines with respect to the patterns to be displayed, e.g. to save power
2310/0216	• • Interleaved control phases for different scan lines in the same sub-field, e.g. initialization, addressing and sustaining in plasma displays that are not simultaneous for all scan lines
2310/0218	• • with collection of electrodes in groups for n- dimensional addressing
2310/0221	• • with use of split matrices (<u>G09G 3/3644</u> and <u>G09G 3/3666</u> take precedence)
2310/0224	• • Details of interlacing
2310/0227	• • related to multiple interlacing, i.e. involving more fields than just one odd field and one even field
2310/0229	• • De-interlacing
2310/0232	• • Special driving of display border areas
2310/0235	• Field-sequential colour display
2310/0237	Switching ON and OFF the backlight within one
2210/024	frame
2310/024	• Scrolling of light from the illumination source over the display in combination with the scanning of the display screen
2310/0243	• • Details of the generation of driving signals
2310/0245	Clearing or presetting the whole screen independently of waveforms, e.g. on power-on (G09G 2310/063 takes precedence)
2310/0248	• Precharge or discharge of column electrodes before or after applying exact column voltages
2310/0251	• • Precharge or discharge of pixel before applying new pixel voltage
2310/0254	• • Control of polarity reversal in general, other than for liquid crystal displays
2310/0256	• • • with the purpose of reversing the voltage across a light emitting or modulating element within a pixel
2310/0259	• • • with use of an analog or digital ramp generator in the column driver or in the pixel circuit

2310/0262	• The addressing of the pixel, in a display other than an active matrix LCD, involving the control of two or more scan electrodes or two or more data electrodes, e.g. pixel voltage dependent on signals of two data electrodes
2310/0264	• • Details of driving circuits
2310/0267	• • Details of drivers for scan electrodes, other than drivers for liquid crystal, plasma or OLED displays
2310/027	• • Details of drivers for data electrodes, the drivers handling digital grey scale data, e.g. use of D/A converters
2310/0272	• • Details of drivers for data electrodes, the drivers communicating data to the pixels by means of a current
2310/0275	• • Details of drivers for data electrodes, other than drivers for liquid crystal, plasma or OLED displays, not related to handling digital grey scale data or to communication of data to the pixels by means of a current
2310/0278	• • • Details of driving circuits arranged to drive both scan and data electrodes
2310/0281	• • Arrangement of scan or data electrode driver circuits at the periphery of a panel not inherent to a split matrix structure
2310/0283	• • Arrangement of drivers for different directions of scanning
2310/0286	• • Details of a shift registers arranged for use in a driving circuit
2310/0289	Details of voltage level shifters arranged for use in a driving circuit
2310/0291	• • Details of output amplifiers or buffers arranged for use in a driving circuit
2310/0294	• • Details of sampling or holding circuits arranged for use in a driver for data electrodes
2310/0297	Special arrangements with multiplexing or demultiplexing of display data in the drivers for data electrodes, in a pre-processing circuitry delivering display data to said drivers or in the matrix panel, e.g. multiplexing plural data signals to one D/A converter or demultiplexing the D/A converter output to multiple columns
2310/04	• Partial updating of the display screen
2310/06	• Details of flat display driving waveforms
2310/061	 for resetting or blanking
2310/062	Waveforms for resetting a plurality of scan
2510/002	lines at a time
2310/063	Waveforms for resetting the whole screen at once
2310/065	• Waveforms comprising zero voltage phase or pause
2310/066	• Waveforms comprising a gently increasing or decreasing portion, e.g. ramp
2310/067	• Special waveforms for scanning, where no circuit details of the gate driver are given
2310/068	• Application of pulses of alternating polarity prior to the drive pulse in electrophoretic displays
2310/08	• Details of timing specific for flat panels, other than clock recovery
2320/00	Control of display operating conditions
2320/02	• Improving the quality of display appearance
2320/0204	Compensation of DC component across the pixels
	in flat panels

2320/0209	• Crosstalk reduction, i.e. to reduce direct or
	indirect influences of signals directed to a certain
	pixel of the displayed image on other pixels of
	said image, inclusive of influences affecting pixels in different frames or fields or sub-images
	which constitute a same image, e.g. left and right
	images of a stereoscopic display
2320/0214	• • • with crosstalk due to leakage current of pixel
2520/0214	switch in active matrix panels
2320/0219	• • Reducing feedthrough effects in active matrix
	panels, i.e. voltage changes on the scan electrode
	influencing the pixel voltage due to capacitive
	coupling
2320/0223	Compensation for problems related to R-C delay
	and attenuation in electrodes of matrix panels,
	e.g. in gate electrodes or on-substrate video signal
	electrodes
2320/0228	• Increasing the driving margin in plasma displays
2320/0233	Improving the luminance or brightness uniformity across the screen
2320/0238	
2320/0238	. Improving the black level
2320/0242	Compensation of deficiencies in the appearance of colours
2320/0247	Flicker reduction other than flicker reduction
2320/0247	circuits used for single beam cathode-ray tubes
2320/0252	• • Improving the response speed
2320/0257	Reduction of after-image effects
2320/0261	• • in the context of movement of objects on the
	screen or movement of the observer relative to the
	screen
2320/0266	Reduction of sub-frame artefacts
2320/0271	Adjustment of the gradation levels within the
	range of the gradation scale, e.g. by redistribution
	or clipping
2320/0276	• • • for the purpose of adaptation to the
	characteristics of a display device, i.e. gamma
2320/028	correction
2320/028	• by changing the viewing angle properties, e.g. widening the viewing angle, adapting the viewing
	angle to the view direction
2320/0285	• using tables for spatial correction of display data
2320/029	• by monitoring one or more pixels in the display
	panel, e.g. by monitoring a fixed reference pixel
2320/0295	• • • by monitoring each display pixel
2320/04	• Maintaining the quality of display appearance
2320/041	Temperature compensation
2320/043	Preventing or counteracting the effects of ageing
2320/045	Compensation of drifts in the characteristics of
	light emitting or modulating elements
2320/046	Dealing with screen burn-in prevention or
	compensation of the effects thereof
2320/048	• • • using evaluation of the usage time
2320/06	Adjustment of display parameters
2320/0606	Manual adjustment
2320/0613	• The adjustment depending on the type of the
2220/072	information to be displayed
2320/062	• • • Adjustment of illumination source parameters
2320/0626	• for control of overall brightness
2320/0633	••• by amplitude modulation of the brightness of the illumination source
2320/064	by time modulation of the brightness of the
2320/004	illumination source
2320/0646	Modulation of illumination source brightness
2020/0040	and image signal correlated to each other

2320/0653	Controlling or limiting the speed of brightness
	adjustment of the illumination source
2320/066	for control of contrast
2320/0666	• for control of colour parameters, e.g. colour
	temperature
2320/0673	• for control of gamma adjustment, e.g. selecting
2220/070	another gamma curve
2320/068	• for control of viewing angle adjustment
2320/0686	• with two or more screen areas displaying information with different brightness or colours
2320/0693	Calibration of display systems
2320/00/3	 Arrangements within a display terminal for setting,
2320/00	manually or automatically, display parameters of the
	display terminal
2320/10	• Special adaptations of display systems for operation
	with variable images
2320/103	. Detection of image changes, e.g. determination of
	an index representative of the image change
2320/106	. Determination of movement vectors or equivalent
	parameters within the image
2330/00	Aspects of power supply; Aspects of display
	protection and defect management
2330/02	. Details of power systems and of start or stop of
	display operation
2330/021	• Power management, e.g. power saving
2330/022	• • • in absence of operation, e.g. no data being
0000	entered during a predetermined time
2330/023 2330/024	 using energy recovery or conservation with inductors, other than in the electrode
2550/024	driving circuitry of plasma displays
2330/025	Reduction of instantaneous peaks of current
2330/026	Arrangements or methods related to booting a
	display
2330/027	• Arrangements or methods related to powering off
	a display
2330/028	Generation of voltages supplied to electrode
	drivers in a matrix display other than LCD
2330/04	Display protection
2330/045	• Protection against panel overheating
2330/06	• Handling electromagnetic interferences
	[EMI], covering emitted as well as received electromagnetic radiation
2330/08	• Fault-tolerant or redundant circuits, or circuits in
2330/00	which repair of defects is prepared
2330/10	• Dealing with defective pixels
2330/12	• Test circuits or failure detection circuits included in
	a display system, as permanent part thereof
2340/00	Aspects of display data processing
2340/02	 Handling of images in compressed format, e.g.
2010/02	JPEG, MPEG
2340/04	. Changes in size, position or resolution of an image
2340/0407	• Resolution change, inclusive of the use of
	different resolutions for different screen areas
2340/0414	Vertical resolution change
2340/0421	Horizontal resolution change
2340/0428	Gradation resolution change
2340/0435	Change or adaptation of the frame rate of the
0240/0442	video stream
2340/0442	• Handling or displaying different aspect ratios, or changing the aspect ratio
2340/045	changing the aspect ratioZooming at least part of an image, i.e. enlarging it
23+0/043	or shrinking it

2340/0457	Improvement of perceived resolution by subpixel
	rendering
2340/0464	• • Positioning
2340/0471	• • • Vertical positioning
2340/0478	Horizontal positioning
2340/0485	Centering horizontally or vertically
2340/0492	• Change of orientation of the displayed image, e.g.
	upside-down, mirrored
2340/06	Colour space transformation
2340/08	Monochrome to colour transformation
2340/10	• Mixing of images, i.e. displayed pixel being
	the result of an operation, e.g. adding, on the
	corresponding input pixels
2340/12	• Overlay of images, i.e. displayed pixel being the
	result of switching between the corresponding input pixels
2340/125	*
2340/123	wherein one of the images is motion videoSolving problems related to the presentation of
2340/14	information to be displayed
2340/145	• related to small screens
2340/145	 Determination of a pixel data signal depending on
2540/10	the signal applied in the previous frame
2350/00	Solving problems of bandwidth in display systems
2352/00	Parallel handling of streams of display data
2254/00	
2354/00	Aspects of interface with display user
2356/00	Detection of the display position w.r.t. other
	display screens
2358/00	A mongoments for display data security
2338/00	Arrangements for display data security
2360/00	Aspects of the architecture of display systems
2360/00 2360/02	• Graphics controller able to handle multiple formats,
2360/02	• Graphics controller able to handle multiple formats, e.g. input or output formats
	Graphics controller able to handle multiple formats, e.g. input or output formatsDisplay device controller operating with a plurality
2360/02 2360/04	 Graphics controller able to handle multiple formats, e.g. input or output formats Display device controller operating with a plurality of display units
2360/02	 Graphics controller able to handle multiple formats, e.g. input or output formats Display device controller operating with a plurality of display units Use of more than one graphics processor to process
2360/02 2360/04 2360/06	 Graphics controller able to handle multiple formats, e.g. input or output formats Display device controller operating with a plurality of display units Use of more than one graphics processor to process data before displaying to one or more screens
2360/02 2360/04	 Graphics controller able to handle multiple formats, e.g. input or output formats Display device controller operating with a plurality of display units Use of more than one graphics processor to process data before displaying to one or more screens Power processing, i.e. workload management for
2360/02 2360/04 2360/06	 Graphics controller able to handle multiple formats, e.g. input or output formats Display device controller operating with a plurality of display units Use of more than one graphics processor to process data before displaying to one or more screens Power processing, i.e. workload management for processors involved in display operations, such as
2360/02 2360/04 2360/06 2360/08	 Graphics controller able to handle multiple formats, e.g. input or output formats Display device controller operating with a plurality of display units Use of more than one graphics processor to process data before displaying to one or more screens Power processing, i.e. workload management for processors involved in display operations, such as CPUs or GPUs
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2360/02 2360/04 2360/06 2360/08	 Graphics controller able to handle multiple formats, e.g. input or output formats Display device controller operating with a plurality of display units Use of more than one graphics processor to process data before displaying to one or more screens Power processing, i.e. workload management for processors involved in display operations, such as CPUs or GPUs Display system comprising arrangements, such as a coprocessor, specific for motion video images
2360/02 2360/04 2360/06 2360/08 2360/10	 Graphics controller able to handle multiple formats, e.g. input or output formats Display device controller operating with a plurality of display units Use of more than one graphics processor to process data before displaying to one or more screens Power processing, i.e. workload management for processors involved in display operations, such as CPUs or GPUs Display system comprising arrangements, such as a
2360/02 2360/04 2360/06 2360/08 2360/10 2360/12	 Graphics controller able to handle multiple formats, e.g. input or output formats Display device controller operating with a plurality of display units Use of more than one graphics processor to process data before displaying to one or more screens Power processing, i.e. workload management for processors involved in display operations, such as CPUs or GPUs Display system comprising arrangements, such as a coprocessor, specific for motion video images Frame memory handling using a cache memory
2360/02 2360/04 2360/06 2360/08 2360/10 2360/12 2360/121	 Graphics controller able to handle multiple formats, e.g. input or output formats Display device controller operating with a plurality of display units Use of more than one graphics processor to process data before displaying to one or more screens Power processing, i.e. workload management for processors involved in display operations, such as CPUs or GPUs Display system comprising arrangements, such as a coprocessor, specific for motion video images Frame memory handling using a cache memory Tiling
2360/02 2360/04 2360/06 2360/08 2360/10 2360/12 2360/121 2360/122	 Graphics controller able to handle multiple formats, e.g. input or output formats Display device controller operating with a plurality of display units Use of more than one graphics processor to process data before displaying to one or more screens Power processing, i.e. workload management for processors involved in display operations, such as CPUs or GPUs Display system comprising arrangements, such as a coprocessor, specific for motion video images Frame memory handling using a cache memory Tiling using interleaving
2360/02 2360/04 2360/06 2360/08 2360/10 2360/12 2360/121 2360/122 2360/123	 Graphics controller able to handle multiple formats, e.g. input or output formats Display device controller operating with a plurality of display units Use of more than one graphics processor to process data before displaying to one or more screens Power processing, i.e. workload management for processors involved in display operations, such as CPUs or GPUs Display system comprising arrangements, such as a coprocessor, specific for motion video images Frame memory handling using a cache memory Tiling using interleaving using unified memory architecture [UMA]
2360/02 2360/04 2360/06 2360/08 2360/10 2360/12 2360/121 2360/122 2360/123 2360/125	 Graphics controller able to handle multiple formats, e.g. input or output formats Display device controller operating with a plurality of display units Use of more than one graphics processor to process data before displaying to one or more screens Power processing, i.e. workload management for processors involved in display operations, such as CPUs or GPUs Display system comprising arrangements, such as a coprocessor, specific for motion video images Frame memory handling using a cache memory Tiling using interleaving
2360/02 2360/04 2360/06 2360/08 2360/10 2360/12 2360/121 2360/122 2360/123 2360/125	 Graphics controller able to handle multiple formats, e.g. input or output formats Display device controller operating with a plurality of display units Use of more than one graphics processor to process data before displaying to one or more screens Power processing, i.e. workload management for processors involved in display operations, such as CPUs or GPUs Display system comprising arrangements, such as a coprocessor, specific for motion video images Frame memory handling using a cache memory Tiling using interleaving using unified memory architecture [UMA] The frame memory having additional data ports,
2360/02 2360/04 2360/06 2360/08 2360/10 2360/12 2360/121 2360/122 2360/123 2360/125	 Graphics controller able to handle multiple formats, e.g. input or output formats Display device controller operating with a plurality of display units Use of more than one graphics processor to process data before displaying to one or more screens Power processing, i.e. workload management for processors involved in display operations, such as CPUs or GPUs Display system comprising arrangements, such as a coprocessor, specific for motion video images Frame memory handling using a cache memory Tiling using unified memory architecture [UMA] The frame memory having additional data ports, not inclusive of standard details of the output serial port of a VRAM Updating a frame memory using a transfer of data
2360/02 2360/04 2360/06 2360/08 2360/10 2360/12 2360/121 2360/122 2360/123 2360/125 2360/126	 Graphics controller able to handle multiple formats, e.g. input or output formats Display device controller operating with a plurality of display units Use of more than one graphics processor to process data before displaying to one or more screens Power processing, i.e. workload management for processors involved in display operations, such as CPUs or GPUs Display system comprising arrangements, such as a coprocessor, specific for motion video images Frame memory handling using a cache memory Tiling using unified memory architecture [UMA] The frame memory having additional data ports, not inclusive of standard details of the output serial port of a VRAM Updating a frame memory using a transfer of data from a source area to a destination area
2360/02 2360/04 2360/06 2360/08 2360/10 2360/12 2360/121 2360/122 2360/123 2360/125 2360/126	 Graphics controller able to handle multiple formats, e.g. input or output formats Display device controller operating with a plurality of display units Use of more than one graphics processor to process data before displaying to one or more screens Power processing, i.e. workload management for processors involved in display operations, such as CPUs or GPUs Display system comprising arrangements, such as a coprocessor, specific for motion video images Frame memory handling using a cache memory Tiling using unified memory architecture [UMA] The frame memory having additional data ports, not inclusive of standard details of the output serial port of a VRAM Updating a frame memory using a transfer of data from a source area to a destination area Frame memory using a Synchronous Dynamic
2360/02 2360/04 2360/06 2360/08 2360/10 2360/12 2360/121 2360/122 2360/123 2360/125 2360/125 2360/127	 Graphics controller able to handle multiple formats, e.g. input or output formats Display device controller operating with a plurality of display units Use of more than one graphics processor to process data before displaying to one or more screens Power processing, i.e. workload management for processors involved in display operations, such as CPUs or GPUs Display system comprising arrangements, such as a coprocessor, specific for motion video images Frame memory handling using a cache memory Tiling using unified memory architecture [UMA] The frame memory having additional data ports, not inclusive of standard details of the output serial port of a VRAM Updating a frame memory using a transfer of data from a source area to a destination area Frame memory using a Synchronous Dynamic RAM [SDRAM]
2360/02 2360/04 2360/06 2360/08 2360/10 2360/12 2360/121 2360/122 2360/123 2360/125 2360/125 2360/127	 Graphics controller able to handle multiple formats, e.g. input or output formats Display device controller operating with a plurality of display units Use of more than one graphics processor to process data before displaying to one or more screens Power processing, i.e. workload management for processors involved in display operations, such as CPUs or GPUs Display system comprising arrangements, such as a coprocessor, specific for motion video images Frame memory handling using a cache memory Tiling using unified memory architecture [UMA] The frame memory having additional data ports, not inclusive of standard details of the output serial port of a VRAM Updating a frame memory using a transfer of data from a source area to a destination area Frame memory using a Synchronous Dynamic RAM [SDRAM] Detecting light within display terminals, e.g. using a
2360/02 2360/04 2360/06 2360/08 2360/10 2360/12 2360/121 2360/122 2360/123 2360/125 2360/125 2360/126 2360/127 2360/128 2360/14	 Graphics controller able to handle multiple formats, e.g. input or output formats Display device controller operating with a plurality of display units Use of more than one graphics processor to process data before displaying to one or more screens Power processing, i.e. workload management for processors involved in display operations, such as CPUs or GPUs Display system comprising arrangements, such as a coprocessor, specific for motion video images Frame memory handling using a cache memory Tiling using unified memory architecture [UMA] The frame memory having additional data ports, not inclusive of standard details of the output serial port of a VRAM Updating a frame memory using a transfer of data from a source area to a destination area Frame memory using a Synchronous Dynamic RAM [SDRAM]
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2360/02 2360/04 2360/06 2360/08 2360/10 2360/12 2360/121 2360/122 2360/123 2360/125 2360/125 2360/126 2360/127 2360/128 2360/14	 Graphics controller able to handle multiple formats, e.g. input or output formats Display device controller operating with a plurality of display units Use of more than one graphics processor to process data before displaying to one or more screens Power processing, i.e. workload management for processors involved in display operations, such as CPUs or GPUs Display system comprising arrangements, such as a coprocessor, specific for motion video images Frame memory handling using a cache memory Tiling using unified memory architecture [UMA] The frame memory having additional data ports, not inclusive of standard details of the output serial port of a VRAM Updating a frame memory using a transfer of data from a source area to a destination area Frame memory using a Synchronous Dynamic RAM [SDRAM] Detecting light within display terminals, e.g. using a single or a plurality of photosensors the light conveying information used for selecting or modulating the light emitting or modulating
2360/02 2360/04 2360/06 2360/08 2360/10 2360/12 2360/121 2360/122 2360/123 2360/123 2360/125 2360/125 2360/126 2360/127 2360/128 2360/14 2360/141	 Graphics controller able to handle multiple formats, e.g. input or output formats Display device controller operating with a plurality of display units Use of more than one graphics processor to process data before displaying to one or more screens Power processing, i.e. workload management for processors involved in display operations, such as CPUs or GPUs Display system comprising arrangements, such as a coprocessor, specific for motion video images Frame memory handling using a cache memory Tiling using unified memory architecture [UMA] The frame memory having additional data ports, not inclusive of standard details of the output serial port of a VRAM Updating a frame memory using a transfer of data from a source area to a destination area Frame memory using a Synchronous Dynamic RAM [SDRAM] Detecting light within display terminals, e.g. using a single or a plurality of photosensors the light conveying information used for selecting or modulating the light emitting or modulating element
2360/02 2360/04 2360/06 2360/08 2360/10 2360/12 2360/121 2360/122 2360/123 2360/125 2360/125 2360/126 2360/127 2360/128 2360/14	 Graphics controller able to handle multiple formats, e.g. input or output formats Display device controller operating with a plurality of display units Use of more than one graphics processor to process data before displaying to one or more screens Power processing, i.e. workload management for processors involved in display operations, such as CPUs or GPUs Display system comprising arrangements, such as a coprocessor, specific for motion video images Frame memory handling using a cache memory Tiling using unified memory architecture [UMA] The frame memory having additional data ports, not inclusive of standard details of the output serial port of a VRAM Updating a frame memory using a transfer of data from a source area to a destination area Frame memory using a Synchronous Dynamic RAM [SDRAM] Detecting light within display terminals, e.g. using a single or a plurality of photosensors the light conveying information used for selecting or modulating the light emitting or modulating element the light being detected by light detection
2360/02 2360/04 2360/06 2360/08 2360/10 2360/12 2360/121 2360/122 2360/123 2360/123 2360/125 2360/125 2360/126 2360/127 2360/128 2360/14 2360/141	 Graphics controller able to handle multiple formats, e.g. input or output formats Display device controller operating with a plurality of display units Use of more than one graphics processor to process data before displaying to one or more screens Power processing, i.e. workload management for processors involved in display operations, such as CPUs or GPUs Display system comprising arrangements, such as a coprocessor, specific for motion video images Frame memory handling using a cache memory Tiling using unified memory architecture [UMA] The frame memory having additional data ports, not inclusive of standard details of the output serial port of a VRAM Updating a frame memory using a transfer of data from a source area to a destination area Frame memory using a Synchronous Dynamic RAM [SDRAM] Detecting light within display terminals, e.g. using a single or a plurality of photosensors the light conveying information used for selecting or modulating the light emitting or modulating element

2360/145	• • the light originating from the display screen
2360/147	the originated light output being determined for
	each pixel
2360/148	• • • the light being detected by light detection
	means within each pixel
2360/16	Calculation or use of calculated indices related to
	luminance levels in display data
2360/18	• Use of a frame buffer in a display terminal,
	inclusive of the display panel
2370/00	Aspects of data communication
2370/02	• Networking aspects
2370/022	. Centralised management of display operation, e.g.
	in a server instead of locally
2370/025	LAN communication management
2370/027	Arrangements and methods specific for the
	display of internet documents
2370/04	• Exchange of auxiliary data, i.e. other than image
	data, between monitor and graphics controller
2370/042	• • for monitor identification
2370/045	• • using multiple communication channels, e.g.
2270/047	parallel and serial
2370/047	• • • using display data channel standard [DDC] communication
2370/06	Consumer Electronics Control, i.e. control of
	another device by a display or <u>vice versa</u>
2370/08	• Details of image data interface between the display device controller and the data line driver circuit
2370/10	• Use of a protocol of communication by packets in
	interfaces along the display data pipeline
2370/12	• Use of DVI or HDMI protocol in interfaces along
2270/14	the display data pipeline
2370/14	• Use of low voltage differential signaling [LVDS] for display data communication
2370/16	• Use of wireless transmission of display information
2370/10	 Use of optical transmission of display information
2370/18	 Details of the management of multiple sources of
2370/20	image data
2370/22	• Detection of presence or absence of input display
20,0,22	information or of connection or disconnection of a
	corresponding information source
2370/24	. Keyboard-Video-Mouse [KVM] switch
2280/00	Specific applications
2380/00 2380/02	Specific applicationsFlexible displays
2380/02	Electronic labels
2380/04	 Remotely controlled electronic signs other than
	labels
2380/08	Biomedical applications
2380/10	Automotive applications
2380/12	Avionics applications
2380/14	Electronic books and readers

2380/16 . Digital picture frames